

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,959,356 B2  
APPLICATION NO. : 09/918258  
DATED : October 25, 2005  
INVENTOR(S) : Steve A. Packwood et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At Column 3, line 66, "FIG. 5 is" should be -- FIG. 5A and FIG. 5B are --.

At Column 3, line 66, "diagram that shows" should be -- diagrams that show --.

At Column 9, line 41, "FIG. 5" should be -- FIG. 5A --.

At Column 9, line 44, "FIG. 5" should be -- FIG. 5A --.

At Column 10, line 4, "FIG. 5" should be -- FIG. 5B --.

At Column 10, lines 4-5, "of the amplitude adjustment circuit 102, the" should be -- of the --.

At Column 10, line 10, "FIG. 5" should be -- FIG. 5B --.

At Column 10, line 16, "FIG. 5" should be -- FIG. 5B --.

At Column 10, line 17, "may implemented" should be -- may be implemented --.

At Column 10, line 19, "FIG. 5" should be -- FIG. 5A --.

At Column 10, line 30, "been" should be -- be --.

At Column 13, lines 35-36, "to the send" should be -- to send --.

At Column 13, line 51, "interface" should be --interfaces --.

Signed and Sealed this

Twenty-sixth Day of June, 2007



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*